

Participants:

Faculty from AICTE Institutions / Universities working in related fields are eligible. Participants should bring a letter of nomination from Head of the Institution stating that they are being deputed for the course. The selection is based on first cum first serve basis.

How to apply:

Interested participants should download and submit the complete application form in prescribed format given, which is available online and send it at coordinators mailing address. Filled in application form should be accompanied by a Demand Draft of drawn in favor of "The Principal, Narayana Engineering College, Nellore", payable at Nellore. The registration fee includes the course material, lunch and refreshments.

Registration Fees:

Registration Fee for Each Participant is **Rs.500/-**

Last Date for Registration: **03.04.2021**

Intimation through e-mail:**05.04.2021**

Resource Persons:

Experts from industry and prestigious academic institutions.

Travel and Accommodation:

All the out station participants will be provided accommodation on payment basis in the Guest House of the Institute, subject to availability.

For More Information

Dr. K. Murali , Professor & HOD

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Email: hodece@necn.ac.in

**Dr. K. S. Sagar Reddy, Professor ,ECE
(Mobile : 9704803650)**

**Mr C. Leela Mohan, Assoc. Professor, ECE
(Mobile : 9885628582)**

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Narayana Group of Educational Institutions.

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Dr. K. Murali, HOD, Dept. of ECE

Course Coordinators

Dr. K. S. Sagar Reddy, Professor ,ECE

Mr C. Leela Mohan, Assoc. Professor, ECE

Organizing Committee

Dr. M. Chandramohan Reddy, Professor, ECE

Mr. K. Selvakumarasamy, Associate Professor, ECE

Mr. S. Girish Gandhi, Associate Professor, ECE

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Mr. SD. Bismil Jaffery, Assistant Professor, ECE

**A Five Day FDP on
Latest Trends in VLSI Design
and Implementation using
EDA tools**

(April 06th to 10th, 2021)

Organized by

Department of

**Electronics & Communication Engineering
In Association with**



APPLY VOLT



NARAYANA

**ENGINEERING
COLLEGE::NELLORE
(AUTONOMOUS)**

Narayana Avenue, Muthukur Road, A. K. Nagar,
Nellore-524 004 (A P) Office: 0861-2313847/43

About the Institute:

Narayana Engineering College is one of the premier engineering colleges in Andhra Pradesh sponsored by Narayana group of educational institutions. The college was established in 1998 with a vision to impart quality technical education and is affiliated to JNTU, Ananthapuramu, and completed 23 years of existence in technical education. Narayana Engineering College was conferred "Autonomous" status by UGC with effect from Academic Year 2020-21 for a period of ten years. The college is Permanently Affiliated to JNTUA, Ananthapuramu and is Accredited by NAAC with A+ GRADE and awarded "A" grade by Govt. of AP.

Narayana Engineering College is located in India's southern State, Andhra Pradesh. This college is located near to the National Highway (NH-5) Nellore. The nearest airport is Chennai, 3 hours drive away. The college offers B.Tech. Programmes in 5 faculties of Engineering namely, Electronics & communication, Computer Science, and Electrical & Electronics, The college also offers Post Graduate programmes MCA , MBA. and M.Tech. in VLSI, EPS, &CSE, Our motto is to give the students an excellence in his or her career, our goal is to shape their future in all aspects.

About the Department:

The aim of the department is to produce Electronics & Telecommunication Engineers, who combine academic excellence with practical expertise. The department of Electronics and Communication Engineering came into existence at Narayana engineering college in 1998 by the approval of the All India Council for Technical Education (AICTE).

The department has progressed rapidly and is now regarded as one of the premiere departments, with excellent infrastructure and a highly qualified and dedicated faculty.

At present, the department is offering B.Tech (ECE) and M.Tech (VLSI) with total intake of 180 and 18 respectively. The majority of our students have been well placed in many companies through campus interviews and many of our students are pursuing their higher education at various premier institutions like IIT's, NIT's and also in abroad.

The department is involves in teaching and research in diverse aspects of Communications, VLSI design, Embedded Systems, Signal Processing, Image Processing and Neural Networks. Apart from teaching, the faculty members have been vigorously pursuing research and regularly publishing research papers in various National and International Journals of repute as well as in prestigious conferences.

About FDP:

Chip Design in India has grown by leaps and bounds, thanks to the presence of several multinationals, design services companies and product companies. It translates to a plethora of opportunities for quality VLSI trained talent. This FDP is intended for young faculty members working in engineering colleges and research scholars to make them familiar with latest trends in VLSI Design/Technology and to enrich them with depth and breadth of knowledge. In VLSI design, the methodology makes extensive use of CAD techniques for all the tasks that span design through layout to finally "sign-off the design database" for chip fabrication.

In addition to theoretical knowledge, the aim of this FDP is to equip the participants with hands-on experience in the state-of-art Cadence EDA tools for VLSI Design supporting Analog and Digital Front End and Back End.

**NARAYANA ENGINEERING
COLLEGE::NELLORE**

REGISTRATION FORM

**A Five Day FDP on
Latest Trends in VLSI Design
and Implementation using
EDA tools**

(April 06th to 07th, 2021)

Name:.....

Qualification:.....

Designation:.....

Address for Correspondence.....

.....

.....

E-mail ID:

Mobile Number:

Experience (if any):

Details of Demand Draft:

DD No: Bank : Rs.

Place: Signature of the Applicant

Date: